

A semiconductor device includes: a plurality of function blocks; a plurality of buses, each of which is respectively connected to one of the plurality of function blocks; a plurality of control signal lines, each of which is respectively connected to one of the plurality of function blocks; a main bus; a bus control unit connected to the main bus; a bus division control unit located between the plurality of buses and the main bus, for connecting one of the plurality of buses to the main bus and transmitting a control signal to a corresponding one of the plurality of control signal lines in accordance with a decoded result of information supplied from the bus control unit via the main bus, thereby controlling a corresponding one of the plurality of function blocks.

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